

REMARKS

Claims 1-12 are pending in the application. Claims 1-12 stand rejected. Claim 1 is herein amended by incorporating the subject matter of claim 2. Claim 2 is canceled. As Applicants have presented no new matter which requires no additional searching, and simply combined claim 2 with claim 1, Applicants respectfully submit that the amendment should be entered.

On the Merits

Claims 1-12 stand rejected under 35 U.S.C. §103(a) as being unpatentable over *Shintani* (6,721,018) in view of *Shoda* (Patent No: JP411032267A; Application No: JP09185541) and *Ho* (6,622,307).

Independent Claim 1:

Independent claim 1 requires in part:

a first CPU; and

a second CPU,

the first CPU being in charge of existent station channel search processing on the side of the digital tuner,

the second CPU being in charge of existent station channel search processing on the side of the analog tuner,

a frequency to be received by each tuner being changed independently by each CPU,

channel searches by the first CPU and second CPU being concurrently conducted;

wherein

when the existent station channel search is started,

the first CPU instructs the second CPU to start the existent station channel search and performs the existent station channel search processing on the side of the digital tuner, and

the second CPU performs the existent station channel search processing on the side of the analog tuner upon receipt of the instruction to start the search from the first CPU.

The Examiner contends that *Shintani* discloses a digital and analog broadcasting receiver which has both a digital and analog tuner as shown in figure 1; reference characters 102 and 101 respectively. The Examiner acknowledges that a single CPU is used in *Shintani* to control both the analog and digital tuners.

The Examiner contends that *Shoda* discloses a first CPU in charge of a first tuner and a second CPU in charge of a second tuner. See drawing 1, reference characters 15 and 54. The Examiner however acknowledges that *Shoda* does not disclose “a frequency to be received by each tuner being changed independently by each CPU,” as required by claim 1.

For this element, the Examiner submits that *Ho* discloses the above feature in figures 1 and 2, reference character 106, and in figure 4A by reference character 153. The reference characters disclose an IRD (integrated receiver/decoder) and a microprocessor respectively.

Ho is directed toward a multiple-room signal distribution system, which “provides digital television programming to a plurality of rooms in a single family dwelling.” Abstract.

The Examiner contends that each IRD in the MIRD (multiple integrated receiver/decoder) has a separate tuner (154) which is controlled by the respective microprocessor (153). Figure 4A shows an example of an IRD. Thus, the Examiner contends that each tuner in each IRD is independently changed by each CPU.

However, Applicants note that the IRD's in *Ho* only appear to have digital tuners. Claim 1 requires both a digital **and analog** tuner to be controlled by their respective CPU's. *Ho* does not disclose any CPU controlling any analog tuner, and therefore the claimed invention is not

disclosed by the cited reference. As such, Applicants respectfully request the rejection to claim 1 be withdrawn and the claim allowed.

Furthermore, claim 1 now requires in part, “...the first CPU instructs the second CPU to start the existent station channel search and performs the existent station channel search processing on the side of the digital tuner...” Emphasis added.

The Examiner contends this feature is disclosed in the primary reference of *Shintani* in column 4, lines 45-52. There *Shintani* states:

When a channel map is to be created, the processor (104) will use the two tuners (101, 102) in parallel to scan the range of available frequencies and locate receivable channels. Under the principles of the present invention, the processor (104) can control the switching circuit (103) and the two tuners (101, 102) so as to simultaneously tune different channels with the two tuners (101, 102).

As is apparent from the above cited disclosure, *Shintani* does not disclose “the first CPU instructs the second CPU to start the existent station channel,” as required by claim 1. Furthermore, *Shintani* cannot disclose this feature because *Shintani* only discloses one CPU. In fact, the Examiner acknowledges in the Office Action that “*Shintani* fails to disclose a second CPU in charge on the side of the analog tuner.” See page 3 of the Office Action.

Additionally, neither *Shoda* or *Ho* disclose or fairly suggest this feature, where the first CPU instructs the second CPU to start the existent station channel search. As such, Applicants respectfully request that the Examiner’s rejection be withdrawn and claim 1 be allowed.

Dependent Claims 9-12:

Claims 9-12 require in part:

the second CPU feeds the channel information obtained in the existent station channel search processing to the first CPU, and

the first CPU manages all the channel information on one memory.

The Examiner contends these features are disclosed in *Shintani* in column 4, lines 45-52 and column 7, lines 7-13. Column 4, lines 45-52 are reproduced above. Column 7, lines 7-13 discloses that the “processor (104) will update the unified channel map memory (206) [as soon as either tuner detects the presence of a valid, receivable channel].”

However *Shintani* does not disclose that “the second CPU feeds the channel information ... to the first CPU,” as required by the claims. As discussed earlier, *Shintani* only discloses 1 CPU. Furthermore, *Shoda* does disclose a second CPU, but does not suggest that the second CPU feeds channel information to the first CPU. In fact, the Examiner acknowledges that *Shoda* does not disclose that different tuners are controlled by different CPUs. See page 4 of the Office Action.

As neither *Shintani*, nor *Shoda* disclose a second CPU in charge of a second tuner, neither of the references can disclose what is claimed in claims 9-12. Furthermore *Ho*, which the Examiner considers to disclose multiple CPUs being in charge of multiple tuners, does not disclose or fairly suggest the second CPU feeding channel information to the first CPU. The CPUs in *Ho* appear to be operating independently based on user input.

As such, Applicants respectfully submit that the claims are in condition for allowance.

Improper Combination

Additionally, the claimed invention is directed toward “a digital and analog **broadcasting** receiver.” The *Ho* reference appears directed toward digital **satellite television**. In other words, the *Ho* reference is not within the same field as the present invention.

Furthermore, the satellite signal in *Ho* reference appears to be only digital, while the claimed invention receives both digital and analog signals. Thus, the fact that *Ho* may teach that a CPU independently changes the frequency to be received by a tuner does not disclose the claimed invention because only digital frequencies appear to be received by *Ho*. The claimed invention receives both digital and analog frequencies. "Each tuner (both digital and analog) being changed independently by each CPU."

As such, Applicants respectfully submit that the *Ho* reference is improperly combined with *Shintani* and *Shoda*.

In view of the aforementioned amendment and accompanying remarks, Applicants submit that the claims, as herein amended, are in condition for allowance. Applicants request such action at an early date.

If the Examiner believes that this application is not now in condition for allowance, the Examiner is requested to contact Applicants' undersigned agent to arrange for an interview to expedite the disposition of this case.

If this paper is not timely filed, Applicants respectfully petition for an appropriate extension of time. The fees for such an extension or any other fees that may be due with respect to this paper may be charged to Deposit Account No. 50-2866.

Respectfully submitted,

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